a first transceiver coupled to the USB host port of said computer; and

a second transceiver coupled to the upstream USB port of said peripheral hub device, said first and second transceivers cooperate to form a wireless USB bus link between said computer and said peripheral hub device.

2. (Amended) A computer system as recited in claim 1, wherein said computer includes a bus controller that controls the wireless USB bus link formed between said computer and said peripheral hub device.

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4. (Amended) A computer system as recited in claim 1, wherein said computer has a housing, and

wherein said first transceiver is provided external to the housing of said computer, and said first transceiver couples to the USB host port of said computer.

Please cancel claim 5 without prejudice.

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6. (Amended) A computer system as recited in claim 4, wherein the [printer] peripheral hub device has a housing, and

wherein said second transceiver is provided internal to the housing of said peripheral hub device.

7. (Amended) A computer system as recited in claim 4, wherein the [printer] peripheral hub device has a housing, and

wherein said second transceiver is provided external to the housing of said peripheral hub device, and said second transceiver couples to the upstream USB port of said peripheral hub device.

8. (Amended) A computer system as recited in claim 1, wherein said peripheral hub device [is a peripheral hub having] has a plurality of downstream USB ports.

Please cancel claim 9 without prejudice.

10. (Amended) A computer system as recited in claim [9] 8, wherein said computer system further comprises a printer, and

wherein said printer is connected to one of the USB downstream ports of the peripheral hub device.

11. (Amended) A computer system as recited in claim 1, wherein said computer supplies a wired, internal USB bus to the USB host port of said computer, and wherein said first transceiver comprises:

a first antenna;

first transceiver circuitry for transmitting data at radio frequencies via said first antenna; and

a first bus interface that interfaces said first transceiver circuitry to the internal USB bus.

23. (Amended) A method for transmitting data over a USB bus from a computer to a peripheral hub device, comprising [the acts of]:

[(a)] providing a first transceiver at the computer, the first transceiver being coupled to a USB host controller that controls a USB bus for the computer;

[(b)] providing a second transceiver at an upstream port of the peripheral hub device; and

[(c)] establishing a wireless USB bus link between the first and second transceivers, the wireless USB bus link being part of the USB bus.

24. (Amended) A method as recited in claim 24, wherein said method further comprises [the acts of]:

[(d)] managing power utilization of the first and second transceivers.